

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Frederic Boutaud

GROUP: 2115

SERIAL NO: 09/981,021

EXAMINER: S. Suryawanshi

FILED: 10/16/01

Date of mailing
of Notice of

FOR: A CIRCUIT AND METHOD FOR PROVIDING
CENTRALIZED SYNCHRONIZATION FOR
THE TRANSPORTATION OF DATA BETWEEN
DEVICES IN DIFFERENT CLOCK DOMAINS
ON A DATA BUS

Allowance: 01/31/05
Confirmation No.: 5638

Mail Stop Issue Fee
Commissioner of Patents
P.O. Box 1450, Alexandria, VA 22313-1450

Attn: Official Draftsman

TRANSMITTAL OF FORMAL DRAWINGS

Applicant submits herewith new drawing(s) for this application. Attached please find seven (7) sheets of formal drawings for this application.


The three month period of response set in the Notice of Allowability (PTOL 37) expires on May 2, 2005, and this submission is on or before this expiry date.

Respectfully submitted,



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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, Mail Stop: Issue Fee.


Sarah E. Kennedy

Date: 2/28/05